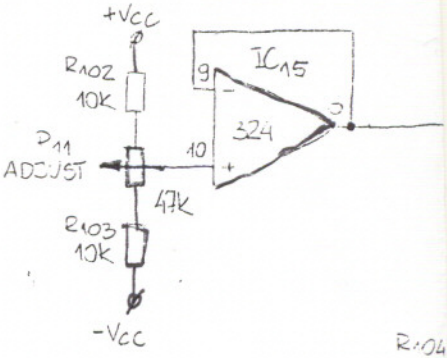
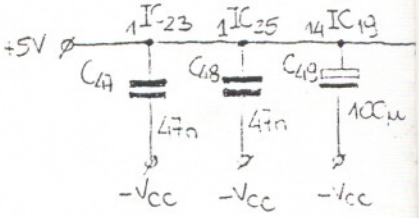
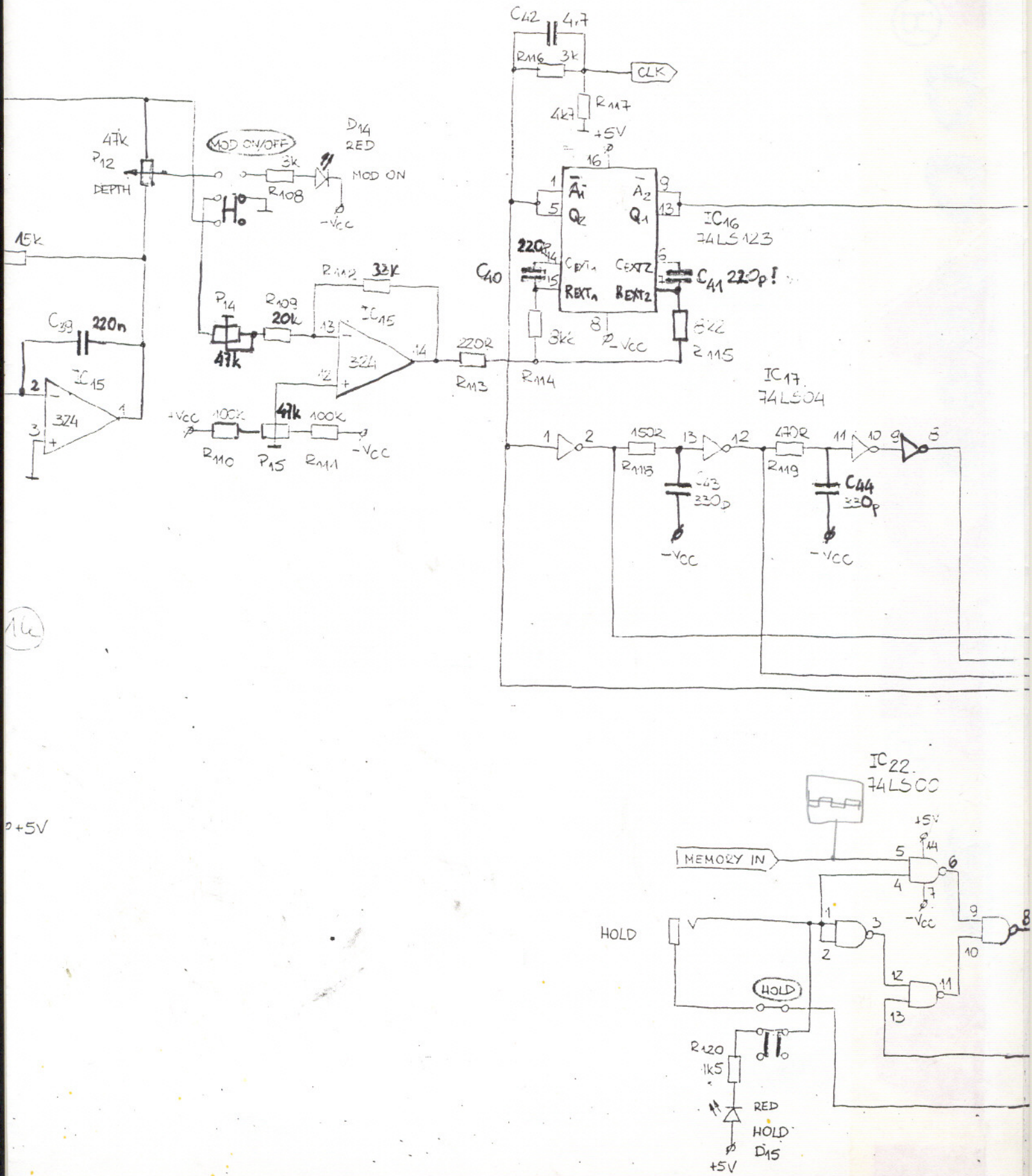


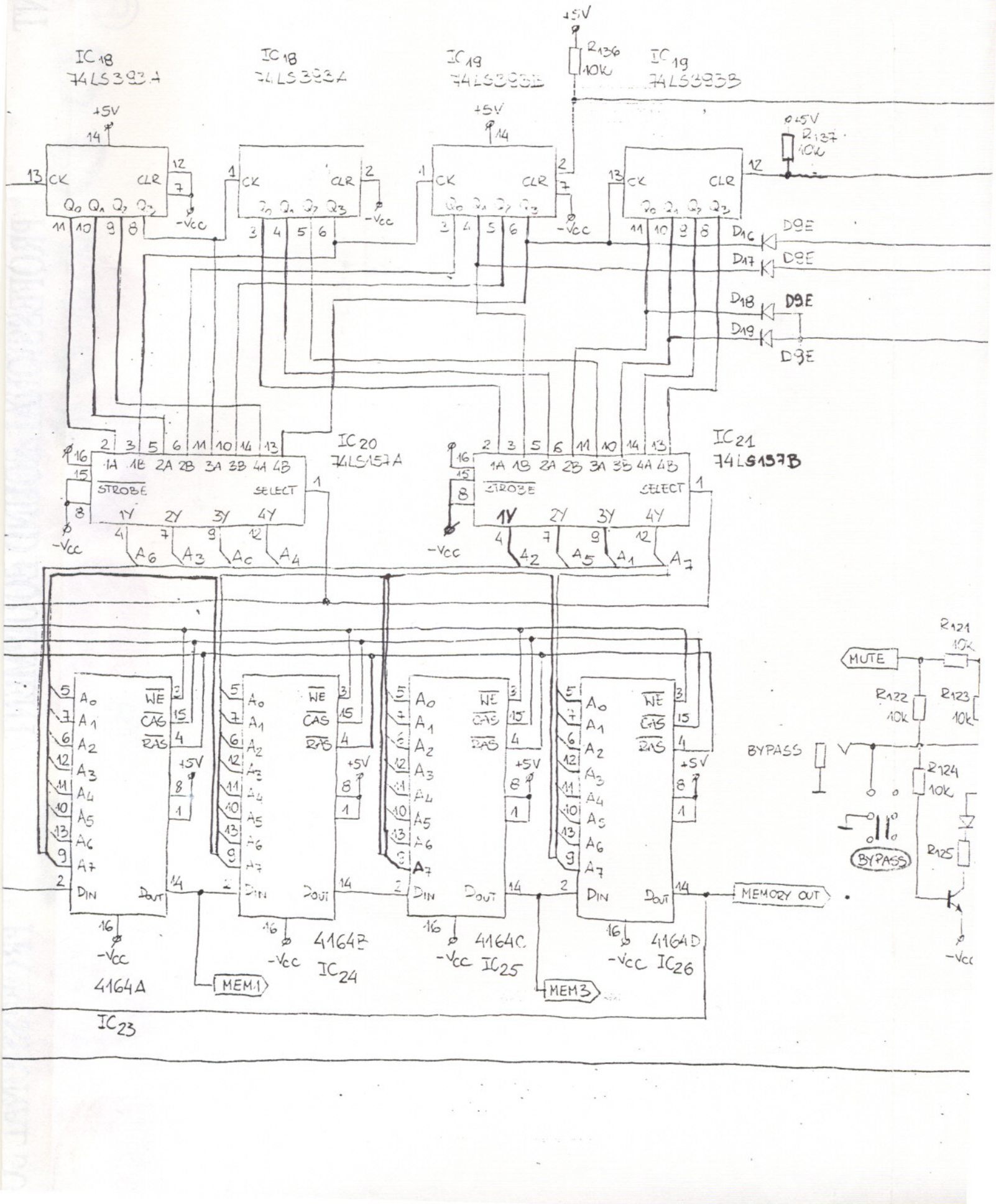
Mit Pot = 100k Ω

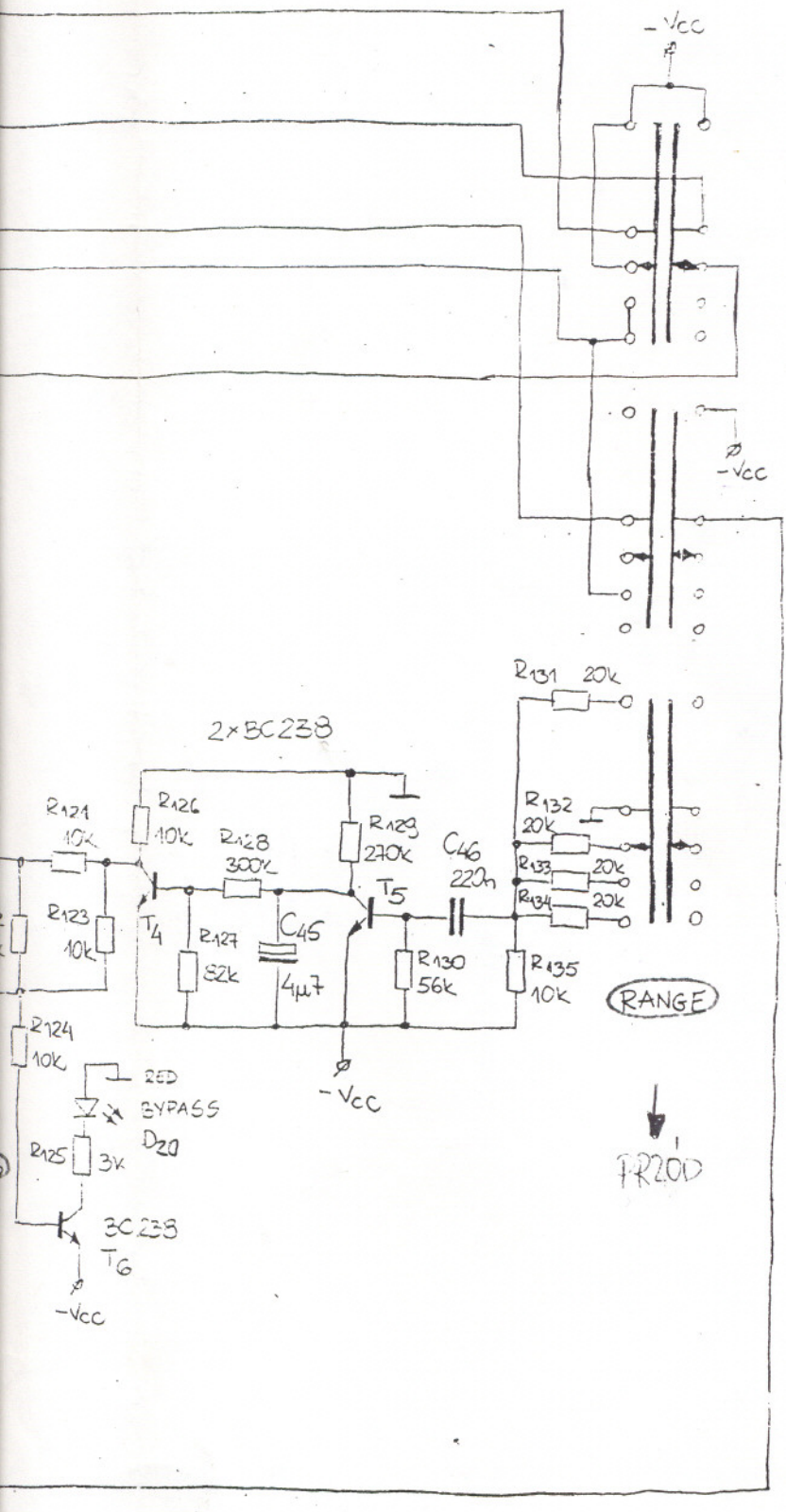
-> R106 (470) ->




C42 4n7
R116 3k
CLK
4k7 R117
+5V
16
1 Ā1 Ā2 9
5 Q2 Q1 13
IC16 74LS123
220R R14
CEXT1 CEXT2
15 6
REXT1 REXT2
C41 220p!
C40
8 -Vcc
3k3 6k2 R115
R114
IC17 74LS04
1 2 150R 13 12 470R 11 10 9 8
R118 C43 330p
R119 C44 330p
-Vcc
47k P12 DEPTH
MOD ON/OFF
3k R108
D14 RED
MOD ON
-Vcc
15k
R112 32k
P14 R109 20k
47k
IC15
C39 220n
2 13 14 220R
324 12
R113
3 1
+Vcc 100k 47k 100k
R110 P15 R111
-Vcc
IC22. 74LS00
+5V
14
MEMORY IN
5 6
4 7
-Vcc
HOLD
1 3 9 8
2 10
12 11
13
HOLD
R120 1k5
RED
HOLD
D15
+5V

DIGITAL DELAY 1000 (2/2)




-Vcc
-Vcc
R131 20k
2×BC238
R121 10k
R126 10k
R128 300k
R129 270k
C46 220n
R132 20k
R133 20k
R134 20k
R123 10k
T4
R127 82k
C45 4µ7
T5
R130 56k
R135 10k
RANGE
R124 10k
RED
BYPASS
D20
R125 3k
BC238
T6
-Vcc
-Vcc
FR20D